

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Steven H. Voldman

Serial No.: 10/604,176

Filed: June 30, 2003

Title: A Method, Apparatus and Circuit for
Latchup Suppression In a Gate-Array
ASIC Environment

IBM Docket No.: BUR920030009US1
(02016-00151)

Group Art Unit: Not yet assigned

Examiner: Not yet assigned

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING (37 C.F.R. 1.8a)

I hereby certify that this correspondence is, on the date shown below, being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, Alexandria, VA 22313-1450.

Anthony P. Gangemi 7/7/2003
Anthony P. Gangemi Date

July 7, 2003

Supplemental Information Disclosure Statement Transmittal

In connection with the above-identified application, please find attached a Supplemental Information Disclosure Statement and copies of the references cited therein.

Respectfully submitted,

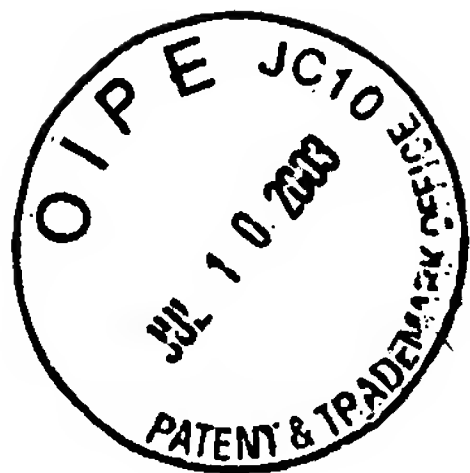
DOWNS RACHLIN MARTIN PLLC

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BTV/245689.1

P242-12/00

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**INFORMATION DISCLOSURE STATEMENT FILED PURSUANT TO THE
DUTY OF DISCLOSURE UNDER 37 C.F.R. §§1.56, 1.97 AND §1.98**

Pursuant to the duty of disclosure under 37 C.F.R. §§1.56, 1.97 and §1.98, applicants request consideration of this Information Disclosure Statement.

Information Cited

Applicants hereby make of record in the above-identified application the information listed on the attached form PTO-1449 (modified). The order of presentation of the references should not be construed as an indication of the importance of the references.

REMARKS

A copy of each of the above-identified items of information is enclosed unless otherwise indicated on the attached form PTO-1449 (modified). It is respectfully requested that:

1. the examiner consider completely the cited information, along with any other information, in reaching a determination concerning the patentability of the present claims;
2. the enclosed form PTO-1449 be signed by the examiner to evidence that the cited information has been fully considered by the Patent and Trademark Office during the examination of this application; and
3. the citations for the information be printed on any patent which issues from this application.

By submitting this Information Disclosure Statement, applicants make no representation that a search has been performed, of the extent of any search performed, or that more relevant information does not exist.

By submitting this Information Disclosure Statement, applicants make no representation that the information cited in this Statement is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

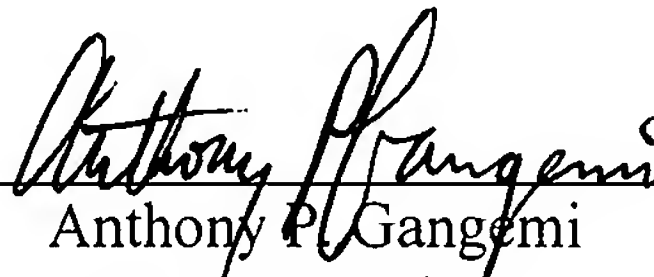
By submitting this Information Disclosure Statement, applicants make no representation that the information cited in this Statement is, or is considered to be, in fact, prior art as defined by 35 U.S.C. §102.

Notwithstanding any statements by applicant, the examiner is urged to form his/her own conclusion regarding the relevance of the cited information.

An early and favorable action is hereby requested.

Respectfully submitted,

DOWNS RACHLIN MARTIN PLLC

By: 
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(802) 863-2375

FORM PTO-1449 (Modified)

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S SUPPLEMENTAL INFORMATION
DISCLOSURE STATEMENT

(Use several sheets if necessary.)



Page 1 of 1

ATTY DOCKET NO.
BUR920030009US1SERIAL NO.
10/604,176

APPLICANT: Steven H. Voldman

FILING DATE:
6/30/2003GROUP:
Not yet assigned

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

| EXAMINERS INITIALS | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE (IF APPRO.) |
|-----------------------|----|--------------------|------|------|-------|----------|----------------------------|
| | AA | | | | | | |
| | AB | | | | | | |
| | AC | | | | | | |
| | AD | | | | | | |
| | AE | | | | | | |
| | AF | | | | | | |
| | AG | | | | | | |
| | AH | | | | | | |

U.S. PATENT APPLICATION PUBLICATIONS

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|--|----|--|--|--|--|--|--|
| | AI | | | | | | |
|--|----|--|--|--|--|--|--|

FOREIGN PATENT DOCUMENTS

| | | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | TRANSLATION | |
|--|----|--------------------|------------|----------------|-------|----------|-------------|----|
| | | | | | | | YES | NO |
| | AJ | GB2286289A | 10/22/1996 | United Kingdom | | | | |
| | AK | DE4108730A1 | 9/24/1992 | Germany | | | | |

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

| | | | | | | | | |
|--|----|--|--|--|--|--|--|--|
| | AL | | Guard Ring Connection In CMOS Structures; H. Hanafi, T. Ludwig, H. Schettler and O. Wagner, IBM Technical Disclosure Bulletin; Vol. 27, No. 11, April 1985; pp. 6806-6807. | | | | | |
| | AM | | | | | | | |

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.